

10734636_CLS1.txt
Most Frequently Occurring Classifications of Patents Returned
From A Search of 10734636 on February 14, 2006

Original Classifications

6	716/16
3	326/39
3	326/73
3	326/83
3	381/106
3	716/18
2	326/126
2	326/66
2	326/71
2	326/81
2	327/108
2	327/12
2	327/565
2	332/103
2	341/156
2	703/14
2	716/1
2	716/11

Cross-Reference Classifications

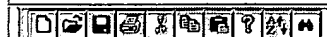
5	326/68
3	326/121
3	326/27
3	326/30
3	326/38
3	326/41
3	326/62
3	330/253
3	330/295
3	333/14
3	375/286
3	703/13
2	326/103
2	326/17
2	326/58
2	326/60
2	326/63
2	326/83
2	326/86
2	327/108
2	327/12
2	327/156
2	327/3
2	327/333
2	327/408
2	327/56
2	327/74
2	331/25
2	341/122
2	365/230.06
2	375/264
2	375/298
2	375/345
2	375/355
2	375/374
2	375/376
2	380/210
2	380/216

2 380/240
 2 381/101
 2 381/109
 2 381/98
 2 703/17
 2 716/12
 2 716/17
 2 716/18
 2 716/6

Combined Classifications

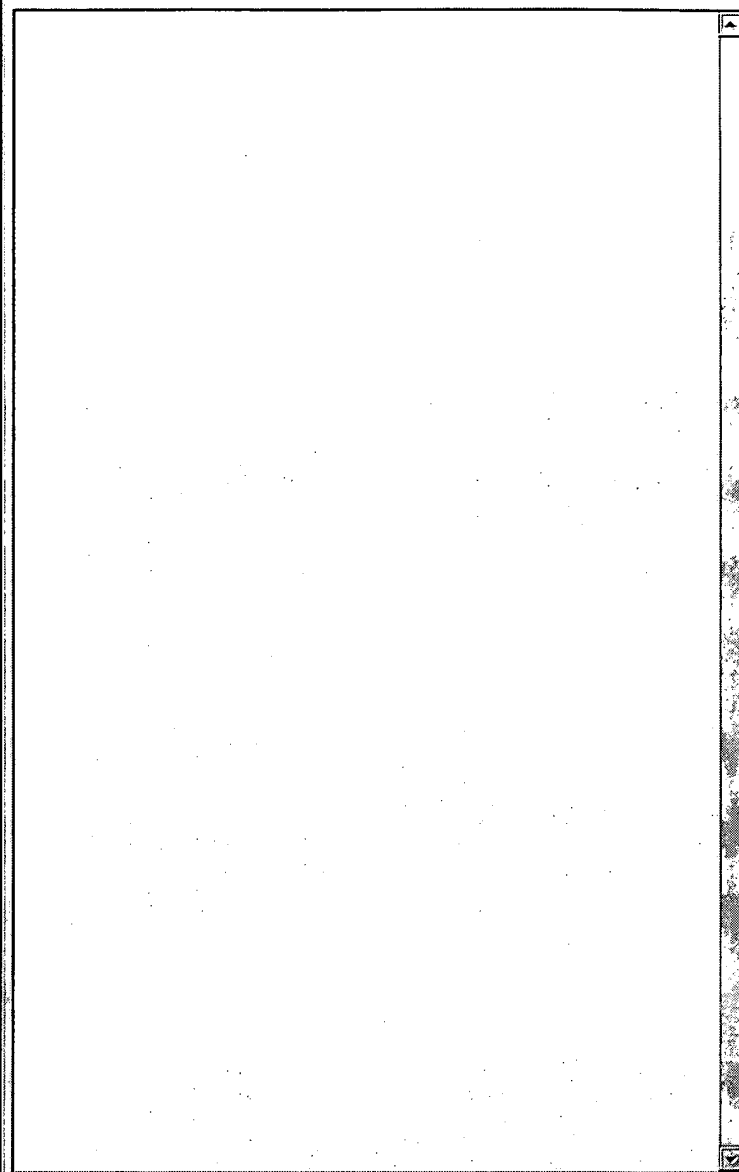
7 716/16
 6 326/68
 5 326/83
 5 716/18
 4 326/39
 4 326/41
 4 327/108
 4 327/12
 4 375/286
 3 326/121
 3 326/27
 3 326/30
 3 326/38
 3 326/60
 3 326/62
 3 326/73
 3 326/81
 3 330/253
 3 330/295
 3 332/103
 3 333/14
 3 341/156
 3 365/230.06
 3 375/264
 3 375/376
 3 381/106
 3 703/13
 3 716/11
 3 716/12
 2 326/103
 2 326/115
 2 326/126
 2 326/17
 2 326/58
 2 326/63
 2 326/66
 2 326/71
 2 326/77
 2 326/82
 2 326/86
 2 327/156
 2 327/205
 2 327/208
 2 327/3
 2 327/333
 2 327/407
 2 327/408
 2 327/411
 2 327/56
 2 327/565
 2 327/74
 2 330/255

2 331/25
2 341/122
2 348/607
2 375/257
2 375/293
2 375/298
2 375/345
2 375/355
2 375/374
2 380/210
2 380/216
2 380/240
2 381/101
2 381/109
2 381/300
2 381/98
2 382/266
2 386/21
2 386/45
2 455/91
2 703/14
2 703/15
2 703/17
2 716/1
2 716/17
2 716/6
2 716/8



- [-] Drafts
 - [-] IS&R:
- [-] Pending
- [-] Active
 - [-] L1: (1) ("6151002").PN.
 - [-] L2: (749) (convert\$5 encod\$4) near9 binary near9 ternary
 - [-] L3: (258) data with 2
- [-] Failed
- [-] Saved
- [-] Favorites
- [-] Tagged (6)
- [-] UDC
- [-] Queue
- [-] Trash

 US-PGPUB: USPAT: USOCR: EPO: JPO: DE
 ☒ Plurals

 Default operator:
☐ Highlight all hit terms initially


	U		Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cla
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 7031403 B2	20060418	17	Phase shift keying modulation including a data converter for	375/308	375/329	Noda; Sa
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5287305 A	19940215	17	Memory device including two-valued/n-valued	365/189.01	365/168	Yoshida;
3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5432735 A	19950711	11	Ternary storage dynamic RAM	365/168	365/189.01	Parks; Te
4	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 4482927 A	19841113	17	Ternary magnetic recording and reproducing system with	360/40	360/39	Melbye;
5	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 3274611 A	19660920	5	Binary to ternary code conversion recording system	360/40	341/57; 375/286	BROWN
6	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5398327 A	19950314	13	Central processing unit including two-valued/n-valued	710/66		Yoshida;

EAST - [10674146.wsp:1]

File View Edit Tools Window Help

☐ Drafts
☐ IS&R:
☐ Pending
☒ Active
☐ L1: (2811) (convert\$4 decod\$4 encod\$4) near9 base near9 (two "2") near9 (three "3")
☐ L2: (70959) base near3 (three "3") near3 (two "2")
☐ L3: (374) 1 same 2
☐ L4: (2131) (convert\$4 decod\$4 encod\$4) near9 base near9 (two "2") near9 (three "3")
☐ L5: (64442) base near3 (three "3") near3 (two "2")
☐ L6: (294) 4 same 5
☐ L7: (96) (326/60).CCLS.
☐ L8: (0) ("binarynear9ternary").PN.
☐ L9: (10699) binary near9 ternary
☐ L10: (423) level near9 9
☐ L11: (214) (data bit signal port terminal) near9 10
☐ L12: (438) (convert\$4 transform\$4) near9 binary near9 ternary near9 (data signal)
☐ L14: (9) 13 not 11
☐ L13: (70) (level base) near9 12
☐ L15: (149) (("4471318") or ("5410719") or ("5544067") or ("5623418") or ("5719802") or ("5801958...))
☐ Failed
☐ Saved
☐ Favorites
☒ Tagged (15)
☐ UDC
☐ Queue
☐ Trash

Search List Browse Queue
 DBs
 Default operator: ifgt eR rms initially
 5990968
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 6239646
 6243183
 6249145
 6253122
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 6275419
 6275411
 6282601
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 6301379

BRS form IS&R form Image

	U		Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cla	
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6249145 B1	20010619	42	Level conversion circuit and se	326/68	326/62;		Tanak
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6243183 B1	20010605	12	Method and equipment for opti	398/209	375/345;		Enfors;
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6239646 B1	20010529	10	High-speed, multiple-input multi	327/407	327/408		Navak
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6222475 B1	20010424	22	Three-step converter	341/156			Okada
5	<input type="checkbox"/>	<input type="checkbox"/>	US 6208845 B1	20010327	36	Frequency modulation method	455/114.2	704/226		Yoshio
6	<input type="checkbox"/>	<input type="checkbox"/>	US 6208193 B1	20010327	7	Multiplexer control scheme	327/411	327/407		Moyal
7	<input type="checkbox"/>	<input type="checkbox"/>	US 6175249 B1	20010116	9	High speed low skew CMOS to E	326/66	326/121;		Good
8	<input type="checkbox"/>	<input type="checkbox"/>	US 6148116 A	20001114	10	Method and circuit for adaptiv	382/266	348/606;		Park; Y
9	<input type="checkbox"/>	<input type="checkbox"/>	US 6137308 A	20001024	16	Programmable interconnect m	326/39	326/38;		Nayak
10	<input type="checkbox"/>	<input type="checkbox"/>	US 6122720 A	20000919	23	Coarse-grained look-up table ar	712/15	711/100		Cliff; R
11	<input type="checkbox"/>	<input type="checkbox"/>	US 6114873 A	20000905	14	Content addressable memory p	326/39	326/41		Sahrae
12	<input type="checkbox"/>	<input type="checkbox"/>	US 6100826 A	20000808	16	Symbol decoding method and	341/67	341/59;		Jeon;
13	<input type="checkbox"/>	<input type="checkbox"/>	US 6094066 A	20000725	15	Tiered routing architecture for fi	326/41			Mavis;
14	<input type="checkbox"/>	<input type="checkbox"/>	US 6091442 A	20000718	12	Apparatus for improving the visi	348/28	348/671		Lee; C
15	<input type="checkbox"/>	<input type="checkbox"/>	US 6088065 A	20000711	24	Contour correction apparatus o	348/625	382/266		Uchide
16	<input type="checkbox"/>	<input type="checkbox"/>	US 6084974 A	20000704	21	Digital signal processing device	381/104	381/107		Niimi; I
17	<input type="checkbox"/>	<input type="checkbox"/>	US 6084476 A	20000704	22	Operational amplifier	330/255	330/253		Hama
18	<input type="checkbox"/>	<input type="checkbox"/>	US 6084436 A	20000704	18	Multi-input semiconductor logic	326/112	257/393;		Komur
19	<input type="checkbox"/>	<input type="checkbox"/>	US 6055418 A	20000425	72	Computer program product co	455/91	330/124R;		Harris;
20	<input type="checkbox"/>	<input type="checkbox"/>	US 6049229 A	20000411	13	Self-biasing CMOS PECL receive	326/83	326/115;		Mano
21	<input type="checkbox"/>	<input type="checkbox"/>	US 6023612 A	20000208	76	Modular transmission system an	455/127.1	330/295;		Harris;
22	<input type="checkbox"/>	<input type="checkbox"/>	US 6018261 A	20000125	11	Method and apparatus for provi	327/307	327/312;		Alford
23	<input type="checkbox"/>	<input type="checkbox"/>	US 6011852 A	20000104	13	Audio power amplifier device fo	381/28	381/101;		Howar
24	<input type="checkbox"/>	<input type="checkbox"/>	US 6008670 A	19991228	14	Differential CMOS logic family	326/115	326/121;		Pace;
25	<input type="checkbox"/>	<input type="checkbox"/>	US 6002861 A	19991214	93	Method for performing simulatio	703/16	703/15;		Butts;
26	<input type="checkbox"/>	<input type="checkbox"/>	US 5990968 A	19991123	22	Video signal processing device	348/537	375/355		Naka;

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File View Edit Tools Window Help

☐ Drafts
 ☐ IS&R:
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 ☒ L1: (2811) (convert\$4 decod\$4 encod\$4) near9 base near9 (two "2") near9 (three "3")
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 ☒ L5: (64442) base near3 (three "3") near3 (two "2")
 ☒ L6: (294) 4 same 5
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 ☒ L8: (0) ("binarynear9ternary").PN.
 ☒ L9: (10699) binary near9 ternary
 ☒ L10: (423) level near9 9
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 ☒ L12: (438) (convert\$4 transform\$4) near9 binary near9 ternary near9 (data signal)
 ☒ L14: (9) 13 not 11
 ☒ L13: (70) (level base) near9 12
 ☐ Failed
 ☐ Saved
 ☐ Favorites
 ☒ Tagged (16)
 ☐ UDC
 ☐ Queue
 ☐ Trash

Search List Browse Queue
QBs ☒ Plurals
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BRS form IS&R form Image

	U		Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cla	
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5271016 A	19931214	18	Shift-correcting code system with	714/752	341/59;		Hilden; H
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20040207430	20041021	20	Input/output interface of an int	326/60	711/154		Jang, Se
3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6339622 B1	20020115	14	Data transmission device	375/287	326/60		Kim; Kuy
4	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6930513 B2	20050816	26	Simultaneous bi-directional sign	326/60	326/59;		Kim; Jin-I
5	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6211698 B1	20010403	10	High speed interface apparatus	326/60	326/59;		Suh; Jun
6	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6140841 A	20001031	10	High speed interface apparatus	326/60	326/59;		Suh; Jun
7	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5912563 A	19990615	10	Trinary signal apparatus and me	326/60	326/62		Garnett;
8	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5498980 A	19960312	7	Ternary/binary converter circuit	326/60	326/59		Bowles; C
9	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5450023 A	19950912	7	Interface circuit using a limited	326/60	326/59		Yang; W
10	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5045728 A	19910903	9	Trinary to binary level conversio	326/60	326/103;		Crafts; H
11	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 4972106 A	19901120	5	Binary-to-ternary converter for	326/60	326/30;		Ruijs; Jar
12	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6963225 B2	20051108	41	Quad state logic design metho	326/59	326/112;		Whetzel;
13	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6133754 A	20001017	150	Multiple-valued logic circuit arc	326/59	326/112;		Olson; E
14	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6477205 B1	20021105	22	Digital data transmission via mult	375/259	326/60		Doblar; D
15	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6232894 B1	20010515	51	Reproducible data conversion	341/83	708/493		Kawasak
16	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 4506252 A	19850319	12	Ternary data encoding system	341/57	360/39;		Jacoby;

Hits Details HTML

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File View Edit Tools Window Help

Drafts
IS&R:
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L1: (2811) (convert\$4 decod\$4 encod
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L3: (374) 1 same 2
L4: (2131) (convert\$4 decod\$4 encod
L5: (64442) base near3 (three "3") nea
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L12: (438) (convert\$4 transform\$4) ne
L14: (9) 13 not 11
L13: (70) (level base) near9 12
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Search List Browse Queue Clear

DBs US-PGPUB:USPAT:USOCR:EPO:JPO:DERWENT:IBM_TDB

Default operator: OR

☒ Plurals
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5	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6211698 B1	20010403	10	High speed interface apparatus	326/60	326/59;		Suh; Jun
6	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6140841 A	20001031	10	High speed interface apparatus	326/60	326/59;		Suh; Jun
7	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5912563 A	19990615	10	Trinary signal apparatus and me	326/60	326/62		Garnett;
8	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5498980 A	19960312	7	Ternary/binary converter circuit	326/60	326/59		Bowles; C
9	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5450023 A	19950912	7	Interface circuit using a limited	326/60	326/59		Yang; W
10	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5045728 A	19910903	9	Trinary to binary level conversio	326/60	326/103;		Crafts; H
11	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 4972106 A	19901120	5	Binary-to-ternary converter for	326/60	326/30;		Ruijs; Jar
12	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6963225 B2	20051108	41	Quad state logic design metho	326/59	326/112;		Whetsel;
13	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6133754 A	20001017	150	Multiple-valued logic circuit arc	326/59	326/112;		Olson; Ec
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16	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 4506252 A	19850319	12	Ternary data encoding system	341/57	360/39;		Jacoby;